

PXAX270 GPIO functions																				
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Balloon 3 allocation Cells highlighted in red indicate the chosen function for that GPIO pin																				
GPIO	VCC	Name	Pin	Net	Wake-up	WU deep	Alt 1 IN	Function	Alt 2 IN	Function	Alt 3 IN	Function	Alt 1 OUT	Function	Alt 2 OUT	Function	Alt 3 OUT	Function	Notes	
0	REG	GPIO<0>	U20	AUX_UNSPEND	X	X													AUX_UNSPEND	
1	REG	GPIO<1>/nRESET_GPIO	U21	PINKO_UNSPEND	X	X													PINKO_UNSPEND	
2	REG	SYS_EN	V21	SYS_EN																
3	REG	GPIO<3>/PWR_SCL	V22	PWR_SCL	X	X														
4	REG	GPIO<4>/PWR_SDA	T19	PWR_SDA	X															
5	REG	PWR_CAP<0>	Y19	NetC3_2																
6	REG	PWR_CAP<1>	AA21	NetC3_1																
7	REG	PWR_CAP<2>	Y18	NetC2_2																
8	REG	PWR_CAP<3>	W17	NetC2_1																
9	REG	GPIO<9>	T22	GPIO9	X						FFCTS	FFUART	HZ_CLK	Clock						
10	REG	GPIO<10>	U22	GPIO10	X		FFDCD				USB_P3_5	USBPHY	HZ_CLK	Clock					CHOUT<0>	
11	IO	GPIO<11>	A8	CLK_48M	X		EXT_SYNC<0>	Timer	SSPRXD2	SSP2	USB_P3_1	USBPHY	CHOUT<0>	Timer	PWM_OUT<2>	PWM			CHOUT<1>	
12	IO	GPIO<12>	A7	CIF_DD7	X		EXT_SYNC<1>	Timer	CIF_DD<7>	Camera			CHOUT<1>	Timer	PWM_OUT<3>	PWM			48_MHz	
13	IO	GPIO<13>	A6	MSL_CLK_EXT	X		CLK_EXT	MSL?	KP_DKIN<7>	Keypad dir	KP_MKIN<7>	Keypad m	SSPTXD2							Clock
14	LCD	GPIO<14>	P20	LCD_VSYNC	X		L_VSYNC	LCD	SSPSFRM2	SSP2					SSPSFRM2	SSP2	UCLK	USIM	Clock	
15	MEM	GPIO<15>	A4	SA_NCS1	X								nPCE<1>	PCMCIA	nCS<1>	Memory				
16	IO	GPIO<16>	C14	COM2_FFTXD	X		KP_MKIN<5>	Keypad matrix							PWM_OUT<0>	PWM	FFTXD	FFUART		
17	IO	GPIO<17>	D15	CIF_DD6	X		KP_MKIN<6>	Keypad m	CIF_DD<6>	Camera					PWM_OUT<1>	PWM				
18	MEM	GPIO<18>	D9	SA_RDY			RDY	Memory												
19	LCD	GPIO<19>	P19	LCD_CS	X		SSPSCLK2	SSP2			FFRXD	FFUART	SSPSCLK2	SSP2	L_CS	LCD	nURST	USIM		
20	MEM	GPIO<20>	W5	SA_NSDCS2			DREQ<0>	DMA	MBREQ	Memory			nSDCS<2>	Memory						
21	MEM	GPIO<21>	Y6	SA_NSDCS3									nSDCS<3>	Memory	DVAL<0>	Memory	MBGNT	Memory		
22	IO	GPIO<22>	A12	RUN_232			SSPEXTCLK2	SSP2	SSPSCLK2EN	SSP2	SSPSCLK2	SSP2	KP_MKOUT<7>	Keypad m	SSPSYSCLK2	SSP2	SSPSCLK2	SSP2	RUN_232	
23	IO	GPIO<23>	A16	CIF_MCLK					SSPSCLK	SSP1			CIF_MCLK	Camera	SSPSCLK	SSP1				
24	IO	GPIO<24>	B14	CIF_FV			CIF_FV	Camera	SSPSFRM	SSP1			CIF_FV	Camera	SSPSFRM	SSP1				
25	IO	GPIO<25>	A15	CIF_LV			CIF_LV	Camera					CIF_LV	Camera	SSPTXD	SSP1				
26	IO	GPIO<26>	A14	CIF_PCLK			SSPRXD	SSP1	CIF_PCLK	Camera	FFCTS	FFUART								
27	IO	GPIO<27>	C13	CIF_DD0			SSPEXTCLK	SSP1	SSPSCLKEN	SSP1	CIF_DD<0>	Camera	SSPSYSCLK	SSP1						
28	IO	GPIO<28>	D12	AC97_BITCLK			AC97_BITCLK	AC97	I2S_BITCLK	I2S	SSPSFRM	SSP1	I2S_BITCLK	I2S	AC97	SSPSFRM	SSP1	SSP1	FFUART	
29	IO	GPIO<29>	A11	AC97_SDATA_IN0			AC97_SDATA_IN_0	AC97	I2S_SDATA_IN	I2S	SSPSFRM	SSP1	I2S_SDATA_OUT	I2S	AC97	SSPSFRM	SSP1	SSP1		
30	IO	GPIO<30>	B11	AC97_SDATA_OUT									I2S_SDATA_OUT	I2S	AC97_SDATA_OUT	AC97	SSPSFRM	SSP1	USBPHY	
31	IO	GPIO<31>	C11	AC97_SYNC	X								I2S_SYNC	I2S	AC97_SYNC	AC97	SSPSFRM	SSP1	USBPHY	
32	IO	GPIO<32>	C16	MMCLK									MSSCLK	Memory	MMCLK	MMC				
33	MEM	GPIO<33>	C7	SA_NCS5			FFRXD	FFUART	FFDSR	FFUART			DVAL<1>	Memory	nCS<5>	Memory	MBGNT	Memory		
34	IO	GPIO<34>	B19	USBP2_2	X		FFRXD	FFUART	KP_MKIN<3>	Keypad m	SSPSCLK3	SSP3	USB_P2_2	USBPHY						
35	IO	GPIO<35>	D17	USBP2_1	X		FFCTS	FFUART	USB_P2_1	USBPHY	SSPSFRM3	SSP3								
36	IO	GPIO<36>	B13	USBP2_4	X		FFDCD	FFUART	SSPSCLK2	SSP2	KP_MKIN<7>	Keypad m	USB_P2_4	USBPHY	SSPSCLK2	SSP2	Keypad m	SSP2		
37	IO	GPIO<37>	D13	USBP2_8	X		FFDSR	FFUART	SSPSFRM2	SSP2	KP_MKIN<3>	Keypad m	USB_P2_8	USBPHY	SSPSFRM2	SSP2	FFTXD	FFUART		
38	IO	GPIO<38>	A13	USBP2_3	X		FFRXD	FFUART	KP_MKIN<4>	Keypad m	USB_P2_3	USBPHY	SSPTXD3	SSP3	SSPTXD3	SSP3	PWM_OUT<1>	PWM		
39	IO	GPIO<39>	B17	USBP2_6	X		KP_MKIN<4>	Keypad matrix			SSPSFRM3	SSP3	USB_P2_6	USBPHY	FFTXD	FFUART	SSPSFRM3	SSP3		
40	IO	GPIO<40>	C12	USBP2_5	X		SSPRXD2	SSP2			USB_P2_5	USBPHY	KP_MKOUT<6>	Keypad m	FFTRD	FFUART	SSPSCLK3	SSP3		
41	IO	GPIO<41>	A19	USBP2_7			FFRXD	FFUART	USB_P2_7	USBPHY	SSPRXD3	SSP3	KP_MKOUT<7>	Keypad m	FFRTS	FFUART				
42	IO	GPIO<42>	D20	COM1_BTRXD			BTRXD	Bluetooth												
43	IO	GPIO<43>	B21	COM1_BTTXD							CIF_FV	Camera	ICP_TXD	IrDA	BTTXD	Bluetooth			CIF_MCLK	
44	IO	GPIO<44>	A20	COM1_BTCTS			BTCTS	Bluetooth			CIF_LV	Camera							CIF_FV	
45	IO	GPIO<45>	C17	COM1_BTRTS							CIF_PCLK	Camera	AC97_SYSCLK	AC97	BTRTS	Bluetooth			CIF_LV	
46	IO	GPIO<46>	A9	COM3_STD_RXD			ICP_RXD	IrDA	STD_RXD	STDUART					PWM_OUT<2>	PWM			Camera	
47	IO	GPIO<47>	C10	COM3_STD_TXD			CIF_DD<0>	Camera					STD_TXD	STDUART	ICP_TXD	IrDA			PWM_OUT<3>	
48	BB	GPIO<48>	Y11	CF_NPOE			CIF_DD<5>	Camera					BB_OB_DAT<1>	MSL	nPOE	PCMCIA				
49	MEM	GPIO<49>	D10	CF_NPWE											nPWE	PCMCIA				
50	BB	GPIO<50>	W12	CF_NPIOR			CIF_DD<3>	Camera			SSPSCLK2	SSP2	BB_OB_DAT<2>	MSL	nPIOR	PCMCIA				
51	BB	GPIO<51>	AB12	CF_NPIOW			CIF_DD<2>	Camera					BB_OB_DAT<3>	MSL	nPIOW	PCMCIA				
52	BB	GPIO<52>	Y12	BB_OB_CLK			CIF_DD<4>	Camera	SSPSCLK3	SSP3										
53	BB	GPIO<53>	AA12	BB_OB_STB	X		FFRXD	FFUART	USB_P2_3	USBPHY			BB_OB_CLK	MSL	SSPSCLK3	SSP3				
54	BB	GPIO<54>	AB13	CF_NPCE2					BB_OB_WAIT	MSL	CIF_PCLK	Camera			nPCE<2>	PCMCIA				
55	BB	GPIO<55>	W9	CF_NPREG			CIF_DD<1>	Camera	BB_IB_DAT<1>	MSL					nREG	PCMCIA				
56	BB	GPIO<56>	AB8	CF_NPWAIT			nPWAIT	PCMCIA	BB_IB_DAT<2>	MSL			USB_P3_4	USBPHY						
57	BB	GPIO<57>	AB9	CF_NIOIS16			nIOIS16	PCMCIA	BB_IB_DAT<3>	MSL										
58	LCD	GPIO<58>	G20	LDD0					LDD<0>	LCD					LDD<0>	LCD				
59	LCD	GPIO<59>	H20	LDD1					LDD<1>	LCD					LDD<1>	LCD				
60	LCD	GPIO<60>	G21	LDD2					LDD<2>	LCD					LDD<2>	LCD				
61	LCD	GPIO<61>	F22	LDD3					LDD<3>	LCD					LDD<3>	LCD				
62	LCD	GPIO<62>	G22	LDD4					LDD<4>	LCD					LDD<4>	LCD				
63	LCD	GPIO<63>	J20	LDD5					LDD<5>	LCD					LDD<5>	LCD				
64	LCD	GPIO<64>	H22	LDD6					LDD<6>	LCD					LDD<6>	LCD				
65	LCD	GPIO<65>	K20	LDD7					LDD<7>	LCD					LDD<7>	LCD				
66	LCD	GPIO<66>	J19	LDD8					LDD<8>	LCD					LDD<8>	LCD				
67	LCD	GPIO<67>	K19	LDD9					LDD<9>	LCD					LDD<9>	LCD				
68	LCD	GPIO<68>	K21	LDD10					LDD<10>	LCD					LDD<10>	LCD				
69	LCD	GPIO<69>	J22	LDD11					LDD<11>	LCD					LDD<11>	LCD				
70	LCD	GPIO<70>	K22	LDD12					LDD<12>	LCD					LDD<12>	LCD				
71	LCD	GPIO<71>	L20	LDD13					LDD<13>	LCD					LDD<13>	LCD				
72	LCD	GPIO<72>	L21	LDD14					LDD<14>	LCD					LDD<14>	LCD				
73	LCD	GPIO<73>	L22	LDD15					LDD<15>	LCD					LDD<15>	LCD				

