



72	32	A9		A9		BP_A9
73	33	A6		A6		BP_A6
74	34	A7		A7		BP_A7
75	35	A4		A4		BP_A4
76	36	A5		A5		BP_A5
77	37	A2		A2		BP_A2
78	38	A3		A3		BP_A3
79	39	A0		A0		BP_A0
80	40	A1		A1		BP_A1
		D				
		Balloon 2	Meaning	Balloon 3		
81	1	VDD_3V		VDD_IO		VCC_IO
82	2	VDD_3V		VDD_IO		VCC_IO
83	3	VDD_3V		VDD_IO		VCC_IO
84	4	VDD_3V		VDD_IO		VCC_IO
85	5	VDD_3V		VDD_IO		VCC_IO
86	6	VDD_3V		VDD_IO		VCC_IO
87	7	VDD_3V		VDD_IO		VCC_IO
88	8	VDD_3V		VDD_IO		VCC_IO
89	9	VDD_5V		VDD_RAW		VDD_RAW
90	10	VDD_5V		VDD_RAW		VDD_RAW
91	11	VDD_5V		VDD_RAW		VDD_RAW
92	12	VDD_5V		VDD_RAW		VDD_RAW
93	13	VDD_5V		VDD_RAW		VDD_RAW
94	14	VDD_5V		VDD_RAW		VDD_RAW
95	15			CIF_DD<9>	GPIO106	CIF_DD9
96	16			CIF_DD<8>	GPIO107	CIF_DD8
97	17			MBREQ/DREQ<0>	GPIO20	BP_DREQ0
98	18			CIF_DD<7>	GPIO12	CIF_DD7
99	19			MBGNT/DVAL<0>	GPIO21	BP_DVAL0
100	20			CIF_DD<6>	GPIO17	CIF_DD6
101	21	EXT_RDY	protected version of RDY	EXT_RDY	GPIO18	BP_EXT_RDY
102	22			CIF_DD<5>	GPIO91	CIF_DD5
103	23	SDCLK1		SDCLK1		BP_SDCLK1
104	24			CIF_DD<4>	GPIO90	CIF_DD4
105	25	NCS2		NCS2	GPIO78	BP_NCS2
106	26			CIF_DD<3>	GPIO115	CIF_DD3
107	27	NCS3		NCS3	GPIO79	BP_NCS3
108	28			CIF_DD<2>	GPIO116	CIF_DD2
109	29	NCS4		NCS4	GPIO80	BP_NCS4
110	30			CIF_DD<1>	GPIO114	CIF_DD1
111	31	NCS5		NCS5	GPIO33	BP_NCS5
112	32			CIF_DD<0>/SSPEXTCLK	GPIO27	CIF_DD0
113	33	RDNWR		RDNWR		BP_RD_NWR
114	34			CIF_MCLK/SSPSCLK	GPIO23	CIF_MCLK
115	35	RDY		RDY	GPIO18	BP_EXT_RDY
116	36			CIF_PCLK/SSPRXD	GPIO26	CIF_PCLK
117	37	NWE		NWE		BP_NWE
118	38			CIF_FV/SSPSFRM	GPIO24	CIF_FV
119	39	NOE		NOE		BP_NOE
120	40			CIF_LV/SSPTXD	GPIO25	CIF_LV
		E				
		Balloon 2	Meaning	Balloon 3		
121	1			BT_CTS	GPIO44	COM1_BTCTS
122	2	LCD_BIAS		LCD_BIAS	GPIO77	LCD_BIAS
123	3	NRES_BUTTON	button	NRES_BUTTON		NRES_BUTTON
124	4	LCD_FCLK		LCD_FCLK	GPIO74	LCD_FCLK
125	5	SA_NRES_OUT		NRESET_OUT		BP_NRESET
126	6	LCD_LCLK		LCD_LCLK	GPIO75	LCD_LCLK
127	7	SA_NRESET		NRESET		BP_NRESET
128	8	LCD_PCLK		LCD_PCLK_OUT	<on FPGA>	LPCLK_OUT
129	9	FRM_TO_CODEC	audio	I2S_SYNC/AC97_SYNC	GPIO31	AC97_SYNC
130	10	LCD_D0		LCD_D0	GPIO58	LDD0
131	11	BCLK_TO_CODEC	audio	I2S_BITCLK/AC97_BITCLK	GPIO28	AC97_BITCLK
132	12	LCD_D1		LCD_D1	GPIO59	LDD1
133	13	D_TO_CODEC	audio	I2S_DATA_OUT/AC97_SDATA_OUT	GPIO30	AC97_SDATA_OUT
134	14	LCD_D2		LCD_D2	GPIO60	LDD2
135	15	D_FROM_CODEC	audio	I2S_DATA_IN/AC97_SDATA_IN_0	GPIO29	AC97_SDATA_IN0
136	16	LCD_D3		LCD_D3	GPIO61	LDD3
137	17	VDD_5V		VDD_RAW		VDD_RAW
138	18	LCD_D4		LCD_D4	GPIO62	LDD4
139	19	VDD_5V		VDD_RAW		VDD_RAW
140	20	LCD_D5		LCD_D5	GPIO63	LDD5
141	21	VDD_3V		VDD_IO		VCC_IO
142	22	LCD_D6		LCD_D6	GPIO64	LDD6
143	23	VDD_3V		VDD_IO		VCC_IO
144	24	LCD_D7		LCD_D7	GPIO65	LDD7
145	25	COM3_TX	console	STD_TXD	GPIO47	COM3_STD_TXD
146	26	EXT_GP0	NPWR_ON (unsuspend)	AUX_UNUSUSP (open drain)	GPIO0	AUX_UNUSPEND
147	27	COM3_RX	console	STD_RXD	GPIO46	COM3_STD_RXD

148	28	EXT_GP1	NAND_SM_CF_OFF (un	n	GPIO9	GPIO9	GPIO9
149	29	COM1_TX	pinko		BT_TXD	GPIO42	COM1_BTTXD
150	30	GND			GND		BP_AUD_IN_GND
151	31	COM1_RX	pinko		BT_RXD	GPIO43	COM1_BTRXD
152	32	MIC_L_RAW		n	AUDIO_IN_L		BP_AUD_IN_L
153	33	COM2_TX	bottom		FF_TXD	GPIO16	COM2_FFTXD
154	34	MIC_R_RAW		n	AUDIO_IN_R		BP_AUD_IN_R
155	35	COM2_RX	bottom		FF_RXD	GPIO96	COM2_FFRXD
156	36	GND			GND		BP_AUD_IN_GND
157	37	UDCNEG			UDCNEG	(USB slave)	UDCNEG
158	38	BATT_FAULT		n	nBATT_FAULT		NBATTFAULT
159	39	UDCPOS			UDCPOS	(USB slave)	UDCPOS
160	40	VDD_FAULT		n	nVDD_FAULT		PWR_OK
		F					
		Balloon 2	Meaning		Balloon 3		
161	1	LCD_D8			LCD_D8	GPIO66	LDD8
162	2	EXT_GP10	FLASH_RDY	n	BT_RTS	GPIO45	COM1_BTRTS
163	3	LCD_D9			LCD_D9	GPIO67	LDD9
164	4	EXT_GP11	I2C_DAT	n	SDA	GPIO118	PXA_SDA
165	5	LCD_D10			LCD_D10	GPIO68	LDD10
166	6	EXT_GP12			MMCLK	GPIO32	MMCLK
167	7	LCD_D11			LCD_D11	GPIO69	LDD11
168	8	EXT_GP13	SSID_ID		MMCMD	GPIO112	MMCMD
169	9	LCD_D12			LCD_D12	GPIO70	LDD12
170	10	EXT_GP14	USB_IRQ	n	AUX_INTERRUPT (open drain)	GPIO94	BP_IRQ
171	11	LCD_D13			LCD_D13	GPIO71	LDD13
172	12	EXT_GP15	NAND_RNB	n	GPIO10	GPIO10	GPIO10
173	13	LCD_D14			LCD_D14	GPIO72	LDD14
174	14	EXT_GP16	CF_ABSENT	n	CF_ABSENT	GPIO105	BP_ABSENT
175	15	LCD_D15			LCD_D15	GPIO73	LDD15
176	16	EXT_GP17	SM_ABSENT	n			
177	17			n	LCD_D16	GPIO86	LDD16
178	18	EXT_GP18	CF_NSTSCNG	n	CF_NSTSCNG	<on FPGA>	BP_NSTSCHG
179	19			n	LCD_D17	GPIO87	LDD17
180	20	EXT_GP19	CODEC_SYSCLK	n	I2S_SYSCLK/AC97_RESET_n	GPIO113	AC97_NRESET
181	21	GND			GND		GND
182	22	EXT_GP20	I2C_CLK	n	SCL	GPIO117	PXA_SCL
183	23	USB_B_UDC+			USB_OTG_UDC+/USB_HOST_UDC_2+		USB_OTG_+
184	24	EXT_GP21	PCMCIA_RDY_NIREQ	n	PCMCIA_RDY_NIREQ	<on FPGA>	BP_NRDY
185	25	USB_B_UDC-			USB_OTG_UDC-/USB_HOST_UDC_2-		USB_OTG_-
186	26	EXT_GP22	NEG_LCD_RUN	n	RUN_BACKLIGHT	GPIO99	RUN_BACKLIGHT
187	27	USB_B_PWR			USB_OTG_VBUS/USB_HOST_PWR_2		USB_OTG_PWR
188	28	EXT_GP23	LCD_PANEL_RUN		RUN_LCD	GPIO93	RUN_LCD
189	29			n	USB_OTG_ID		USB_OTG_ID
190	30	EXT_GP24	RTC_NINT	n	L_VSYNC	GPIO14	LCD_VSYNC
191	31	NRUN_5V		n			
192	32	EXT_GP25	USB_OFF	n	L_CS	GPIO19	LCD_CS
193	33	GND			GND		GND
194	34	EXT_GP26			MMDAT<0>	GPIO92	MMDAT0
195	35	USB_A_UDC+			USB_HOST_UDC_1+	(USB host 1)	USB_HOST_UDC1+
196	36	EXT_GP27	AUDIO_OFF	n	MMDAT<1>	GPIO109	MMDAT1
197	37	USB_A_UDC-			USB_HOST_UDC_1-	(USB host 1)	USB_HOST_UDC1-
198	38			n	MMDAT<2>/MMCCS<0>	GPIO110	MMDAT2
199	39	USB_A_PWR			USB_HOST_PWR		USB_HOST_PWR
200	40			n	MMDAT<3>/MMCCS<1>	GPIO111	MMDAT3